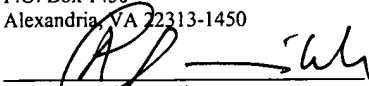


I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to:

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

  
Attorney for Applicant

Date: 30 November 2004



**PATENT**

**Attorney Docket No.: DB000861-002**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>Applicant:</b>	Feng Lin	)	<b>Examiner:</b>	Nitin C. Patel
		)		
<b>Serial No.:</b>	10/731,679	)	<b>Art Unit:</b>	2116
		)		
<b>Filed:</b>	December 9, 2003	)		

**Entitled:** SYSTEM AND METHOD OF OPERATION OF DLL (DUAL LOCKED LOOP) AND PLL (PHASE LOCKED LOOP) TO PROVIDE TIGHT LOCKING WITH LARGE RANGE, AND DYNAMIC TRACKING OF PVT VARIATIONS USING INTERLEAVED DELAY LINES

**TRANSMITTAL LETTER**

**To:** Mail Stop ISSUE FEE  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

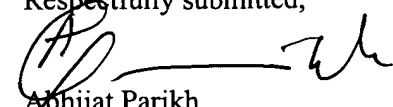
Enclosed for filing are the following:

1. an Issue Fee Transmittal, Part B (PTO Form PTOL-85B) x 2;
2. a Fee Address Indication Form (PTO/SB/47);
3. Remarks to Examiner's Reasons for Allowance; and
4. a check in the amount of \$1,685.00 as the requisite fee under 37 C.F.R. § 1.18(a), §1.18(d), and for five (5) advanced copies of the issued patent.

The Commissioner is hereby authorized to charge any underpayment or credit any overpayment to our Deposit Account No. 20-0888. A copy of this transmittal letter is enclosed.

Also enclosed is a return postcard. Please date stamp and mail the postcard to acknowledge receipt of the above-mentioned correspondence.

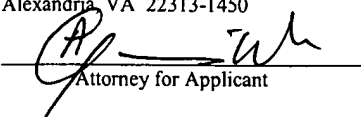
Respectfully submitted,

  
Abhijat Parikh  
Reg. No. 49,086  
Thorp Reed & Armstrong, LLP  
One Oxford Centre, 14<sup>th</sup> Floor  
Pittsburgh, PA 15219-1425  
(412) 394-2478  
Attorneys for Applicant

Date: 30 November 2004

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Date: 30 November 2004



**PATENT**

Attorney Docket No.: DB000861-002

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant:** Feng Lin )  
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**Serial No.:** 10/731,679 )  
 )  
**Filed:** December 9, 2003 )

**Examiner:** Nitin Patel

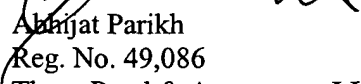
**Art Unit:** 2116

**Title:** SYSTEM AND METHOD OF OPERATION OF DLL (DUAL LOCKED LOOP) AND PLL (PHASE LOCKED LOOP) TO PROVIDE TIGHT LOCKING WITH LARGE RANGE, AND DYNAMIC TRACKING OF PVT VARIATIONS USING INTERLEAVED DELAY LINES

**REMARKS TO EXAMINER'S REASONS FOR ALLOWANCE**

Reasons for allowance are only warranted in instances in which the record of the prosecution as a whole does not make clear the Examiner's reasons for allowing a claim or claims. In the present case, applicant believes the record as a whole does make the reasons for allowance clear and therefore no statement by the Examiner is necessary or warranted. Furthermore, the applicant does not necessarily agree with each statement in the Examiner's Reasons for Allowance.

Respectfully submitted,

  
Abhijit Parikh  
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